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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q77292

Akio SUGIYAMA

Appln. No.: 10/660,518

Group Art Unit: 2811

Confirmation No.: 5305

Examiner: Unknown

Filed: September 12, 2003

For: DATA READ CIRCUIT IN A SEMICONDUCTOR DEVICE FEATURING REDUCED

CHIP AREA AND INCREASED DATA TRANSFER RATE

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith please find 15 sheets of drawings in compliance with

37 C.F.R. § 1.84. The Examiner is respectfully requested to acknowledge receipt of these

drawings.

Respectfully submitted,

Registration No. 25,665

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Date: January 5, 2004